

Simple Calculations Anyone Can Do to Help Ensure EMC Design Compliance




Todd H. Hubing
 Professor Emeritus
 Clemson University

EMC Requirements and Key Design Considerations

Conducted Emissions	Radiated Emissions	Radiated Immunity	Transient Immunity	Electrostatic Discharge	Bulk Current Injection
<ul style="list-style-type: none"> • Grounding • Transition Times • Filter Design • Filter Layout • SMPS Layout • Decoupling 	<ul style="list-style-type: none"> • EMC Ground • Transition Times • Filtered I/O • Adequate Decoupling • Balance Control • Current Return 	<ul style="list-style-type: none"> • EMC Ground • Chassis GND • Filtered I/O • Balance Control • Bandwidth Control • Key Circuit Layout 	<ul style="list-style-type: none"> • HF Current Path • LF Current Path • Chassis GND • Filtered I/O • Transient Protection • Bandwidth Control 	<ul style="list-style-type: none"> • Arc Management • Bandwidth Control • LF Current Path • HF Current Path • Chassis GND • Filtered I/O • Transient Protection 	<ul style="list-style-type: none"> • 1 HF GND • HF Current Path • Chassis GND • Filtered I/O • Identify Key Circuits • Bandwidth Control

Designing a product that is guaranteed to meet all these requirements is relatively straight-forward. Fixing a non-compliant product can be difficult and costly.

What People Who Guarantee First-pass Compliance Are **NOT** Doing



EMC Design Guideline Collection


Over the past 25 years, we've had opportunities to work with a wide variety of companies to solve circuit-board or system-level EMC problems. During this time, we've encountered all kinds of EMC design rules. Some of them are helpful, some not-so-helpful, and some your product will have EMC problems.

We've published our favorite EMC design rule collected primarily from lists maintained by books, technical papers and application notes rules (we prefer to call them "guidelines") on your information and entertainment. We hope you find them helpful.

- Why You Should Be Cautious About
- The Most Important EMC Design Guidelines
- Other Good EMC Design Guidelines
- Not-So-Good EMC Design Guidelines
- Some of the Worst EMC Design Guidelines
- Effective Application of EMC Design Guidelines
- Commercial EMC Rule Checkers

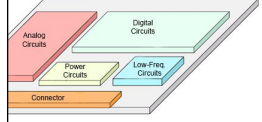
If you have a guideline that you'd be willing to share, please email it to info@LearnEMC.com. Be sure to indicate the type of guideline it is.

Updates or corrections to this web page should be emailed to info@LearnEMC.com.



Some of the Worst EMC Design Guidelines

- ❑ Rules that work well in some designs can be completely inappropriate for other designs.
- ❑ Complying with a long list of EMC design rules is a terrible way to do a board layout.
- ❑ Many rules widely published in books, app notes, and data sheets are NEVER appropriate.



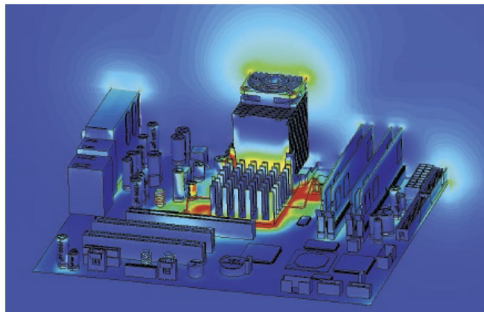
It's a good idea to put the components that send or receive signals through the connector nearest the connector. Placement is important, but design guidelines that dictate placement without considering the function and signals associated with the circuits are very dangerous.

It should be gapped between analog and digital circuits.

Probably a close second in the competition for the worst EMC design guideline every conceived. There are some (very few) situations where gapping a ground plane between analog and digital circuits is a good idea. These situations are

NOT Relying on EMC Design Guidelines

What People Who Guarantee First-pass Compliance Are **NOT** Doing



Numerical EM modeling codes give precise answers to precisely defined problems. EMC geometries are not well-defined.

We don't want to know how much a given configuration will radiate. The answer to that question depends on a lot of factors that we have no control over.

We want to know if our product will meet its requirements.

NOT Modeling Products with Numerical EM Modeling Codes

What People Who Guarantee First-pass Compliance ARE Doing!

The diagram illustrates the relationship between a source, a coupling mechanism, and an antenna. On the left, a light blue box labeled 'SOURCE' is connected to a photograph of a green PCB. A large grey arrow labeled 'COUPLING MECHANISM' points to a light pink box labeled 'ANTENNA'. Below the arrow are four small diagrams showing different types of coupling mechanisms: a microstrip line, a slot, a gap, and a via. To the right of the arrow are three photographs of antennas: a black PCB antenna, a blue PCB antenna with a microstrip line, and a green PCB antenna with a microstrip line.

Identifying and evaluating all possible sources, victims and coupling paths

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What People Who Guarantee First-pass Compliance ARE Doing!

The diagram illustrates the relationship between a source port, a coupling mechanism, and a victim circuit. On the left, a light pink box labeled 'SOURCE PORT' is connected to a photograph of a red PCB with a white connector. A large grey arrow labeled 'COUPLING MECHANISM' points to a light blue box labeled 'VICTIM CIRCUIT'. Below the arrow are three small diagrams showing different types of coupling mechanisms: a microstrip line, a slot, and a gap. To the right of the arrow are three photographs of victim circuits: a black PCB with a microstrip line, a green PCB with a microstrip line, and a green PCB with a microstrip line and a component.

Identifying and evaluating all possible sources, victims and coupling paths

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Why Design for First-Pass Compliance?

Why not build and test a prototype to see what EMC fixes are necessary?

Designing for first-pass compliance results in

- ❑ Better product reliability
- ❑ Faster product development
- ❑ Lower product cost

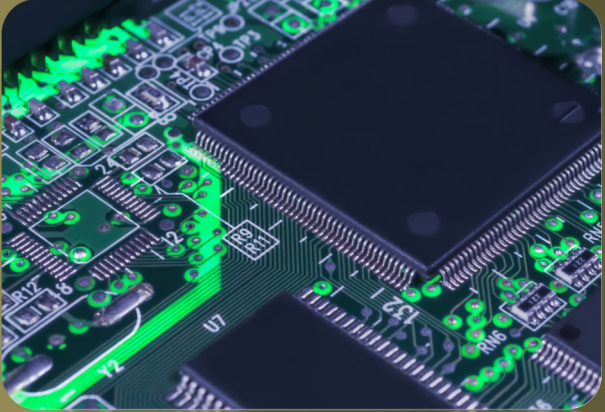


Keys to PCB Design for EMC Compliance

- ❑ Don't rely on EMC Design Guidelines
- ❑ Be familiar with currents and current paths
- ❑ Learn to recognize EMI sources
- ❑ Learn to recognize antennas / ports
- ❑ Be aware of fundamental EMI radiation mechanisms
- ❑ **Quantify the coupling!!!**

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Is an Isolated Current Return Required?

$$V_{\text{coupled}} = I_{\text{source}} \times R_{\text{shared}}$$


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Reasons for Isolating a Current Return

- ❑ Safety
 - ❖ Currents backed by voltage >48 volts cannot flow on easily reached conductors.
 - ❖ Current-return conductors cannot be labeled “ground”.
- ❑ To Avoid Common-Impedance Coupling
 - ❖ Common-Impedance coupling occurs between circuits that share return conductors.
 - ❖ Unfortunately, these current-return conductors are often labeled “ground”.

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Is an Isolated Ground Required?

STM32F407
32-Bit ARM Microcontroller
LQFP64

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Why would a designer isolate an analog return?

There is only one valid reason to isolate current returns!

To prevent low-frequency (kHz or lower) common-impedance coupling.

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Quantifying Maximum Common-Impedance Coupling

$$V_{\text{coupled (worst-case)}} = I_{\text{source (worst-case)}} \times R_{\text{shared (worst-case)}}$$

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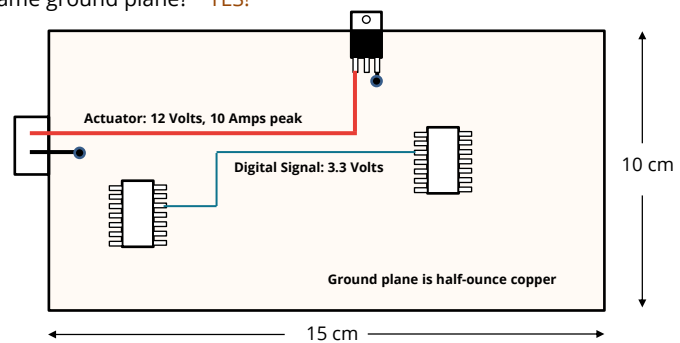
Do these returns need to be isolated?

Don't Split Ground Planes!

Can these signals share the same ground plane? **YES!**

Don't gap the plane!

Don't neglect E-field coupling!



End-to-End resistance of board: 0.86 mΩ

Voltage induced by common-impedance coupling: **< 8.6 mV**

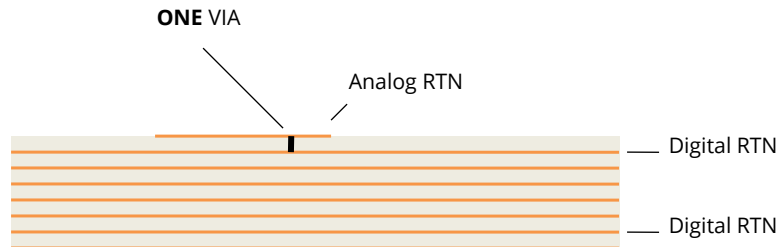
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Don't gap the ground plane!!!

In rare situations where isolated low-frequency returns are required, route the isolated return on a different layer over the ground plane.



Quantifying Common-Impedance Coupling

Maximum common impedance coupling is easy to quantify!

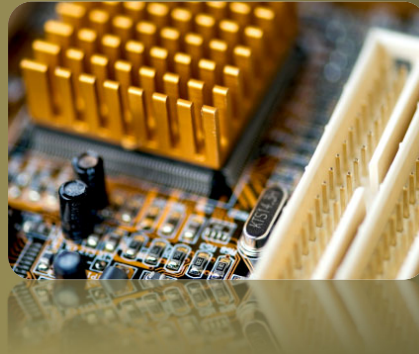
$$V_{\text{coupled (worst-case)}} = I_{\text{source (worst-case)}} \times R_{\text{shared (worst-case)}}$$

NOTE:

Use R_{shared} , not ωL_{shared} or $1/\omega C_{\text{shared}}$. Those values indicate magnetic- or electric-field coupling.

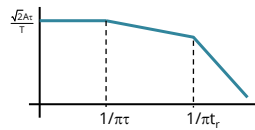
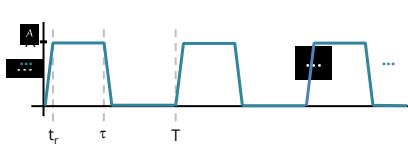
- ❑ Perform this calculation when you suspect conducted coupling will be an issue.
- ❑ ALWAYS perform this calculation before isolating signal returns labeled "ground"!

How Strong is this Noise Source?



Nth Harmonic Amplitude: $V_N = \frac{0.45 V_{PP}}{N}$

How Strong is this Digital Clock Source?



Fundamental Frequency: $f_1 = \frac{1}{T}$

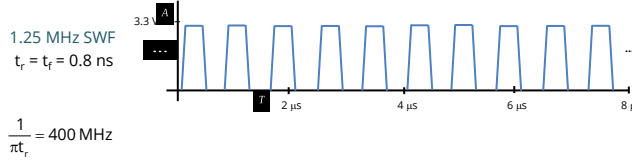
Fundamental Amplitude: $V_1 = 0.45 V_{PP}$

Harmonic Amplitude (rms): $\sqrt{2}|c_n| = \frac{\sqrt{2}A\tau}{T} \left| \frac{\sin(n\pi\tau/T)}{(n\pi\tau/T)} \right| \left| \frac{\sin(n\pi\tau/T)}{(n\pi\tau/T)} \right|$

Nth Harmonic Amplitude: $V_N = \frac{0.45 V_{PP}}{N}$

Envelope Amplitude (rms): $V_{rms-max}(f) = \begin{cases} \frac{\sqrt{2}A\tau}{T} & \text{when } f < \frac{1}{\pi\tau} \\ \frac{\sqrt{2}A}{\pi} \left(\frac{f_0}{f} \right) & \frac{1}{\pi\tau} < f < \frac{1}{\pi\tau_r} \\ \frac{\sqrt{2}A}{\pi^2} \left(\frac{f_0}{f} \right) \left(\frac{1}{t_r f} \right) & \text{when } f > \frac{1}{\pi\tau_r} \end{cases}$

How Strong is this Power Switching Source?



Fundamental Amplitude: $V_1 = 0.45(3.3) \approx 1.5 \text{ V}$

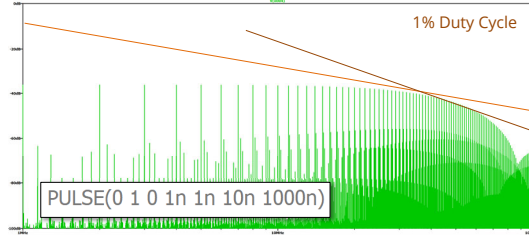
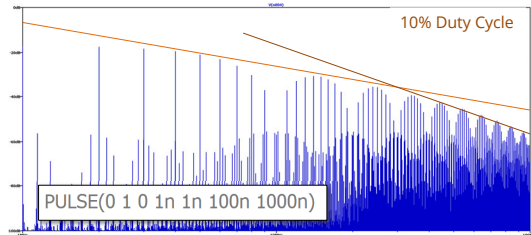
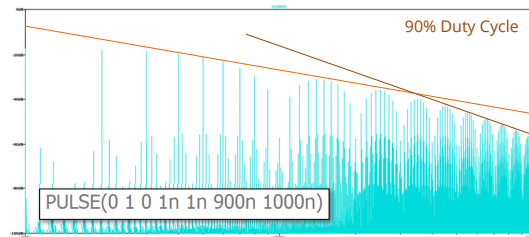
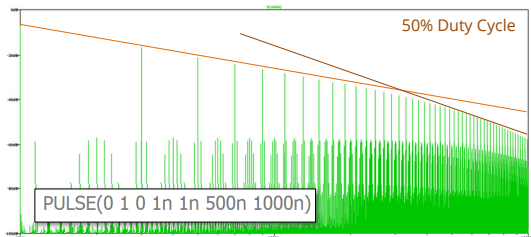
100th Harmonic Amplitude: $V_N = \frac{1.5 \text{ V}}{100} = 15 \text{ mV}$



If max allowed is $100 \mu\text{V}$, then max coupling allowed is:

$$20 \log \left(\frac{100 \mu\text{V}}{15 \text{ mV}} \right) = -44 \text{ dB}$$

Effect of Pulse Width on Frequency Content



Basic Calculations

Noise Voltage from a Switch-Mode Power Supply

Determine the worst-case voltage on the switching voltage node of a buck converter ($V_{in} = 12\text{ V}$, $V_{out} = 5\text{ V}$, $F_{SW} = 2\text{ MHz}$, $t_r = 1.2\text{ ns}$, minimum duty cycle = 20%). Determine the amplitude of the harmonics at 30 MHz and 300 MHz.

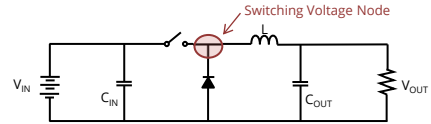
$$\frac{1}{\pi t_r} = \frac{1}{\pi (1.2 \times 10^{-9})} = 265\text{ MHz}$$

@30 MHz:

$$V_{\max}(f) = \frac{\sqrt{2}A}{\pi} \left(\frac{f_0}{f}\right) = \frac{\sqrt{2}(12)}{\pi} \left(\frac{2\text{ MHz}}{30\text{ MHz}}\right) = 360\text{ mV}$$

@300 MHz:

$$V_{\max}(f) = \frac{\sqrt{2}A}{\pi^2} \left(\frac{f_0}{f}\right) \left(\frac{1}{t_r f}\right) = \frac{\sqrt{2}(12)}{\pi^2} \left(\frac{2}{300}\right) \left(\frac{1}{(1.2 \times 10^{-9})(300 \times 10^6)}\right) = 32\text{ mV}$$



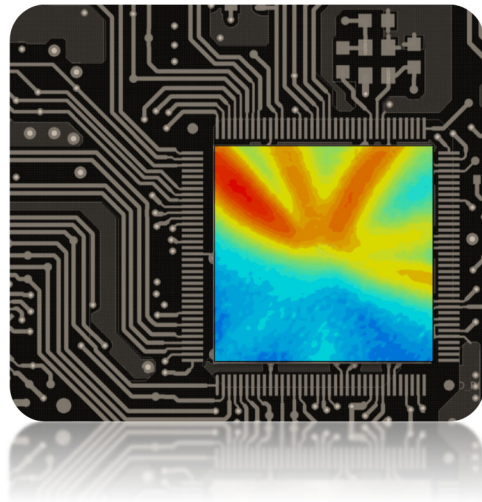
$$V_{\max}(f) = \begin{cases} \frac{\sqrt{2}A\tau}{T} & \text{when } f < \frac{1}{\pi\tau} \\ \frac{\sqrt{2}A}{\pi} \left(\frac{f_0}{f}\right) & \frac{1}{\pi\tau} < f < \frac{1}{\pi t_r} \\ \frac{\sqrt{2}A}{\pi^2} \left(\frac{f_0}{f}\right) \left(\frac{1}{t_r f}\right) & \text{when } f > \frac{1}{\pi t_r} \end{cases}$$

IC Pins as Sources

Treat every pin as if it were the source of the IC's internal clocks.

Fundamental Amplitude: $V_1 = 0.45 V_{pp}$

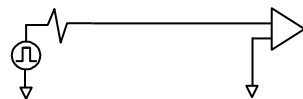
Nth Harmonic Amplitude: $V_N = \frac{0.45 V_{pp}}{N}$



Analog Sources

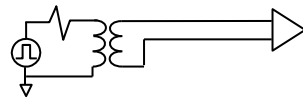
The amplitude and bandwidth of analog sources is generally well-known.

Single-ended vs. Differential Signaling



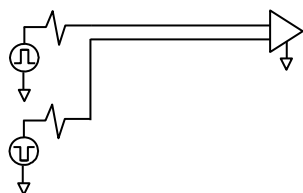
Single-ended

- Unbalanced
- Currents return on "ground"
- Requires N+1 conductors
- Inexpensive parts



Differential

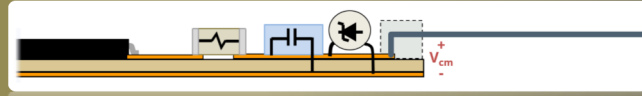
- Balanced
- No signal current on ground
- Requires 2N conductors
- Requires balun



Pseudo-Differential

- Nominally Balanced
- Nominally no HF current on ground
- Always has a CM voltage component!!
- Requires 2N conductors
- No balun required

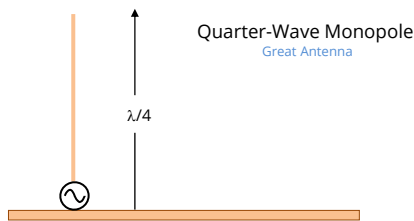
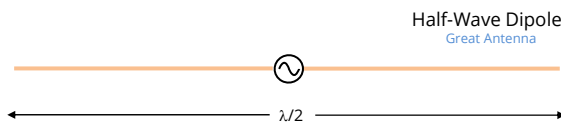
How much common-mode voltage is acceptable?



CISPR 32: $V_{cm} \ll 1\text{mV}$
 Automotive: $V_{cm} < 100\ \mu\text{V}$

Identifying Antennas

What makes an efficient antenna?



Average radiated power required to exceed FCC Class B Limit:

$$P_{rad} = \frac{|E|^2}{\eta} \frac{2\pi r^2}{D_0}$$

$$= \frac{(100\ \mu\text{V}/\text{m})^2}{377\ \Omega} \frac{2\pi(3\ \text{m})^2}{1.6}$$

$$\approx 1\ \text{nW}$$

Voltage driving a resonant monopole required to exceed FCC Class B Limit:

$$V = \sqrt{P_{rad} R_{rad}}$$

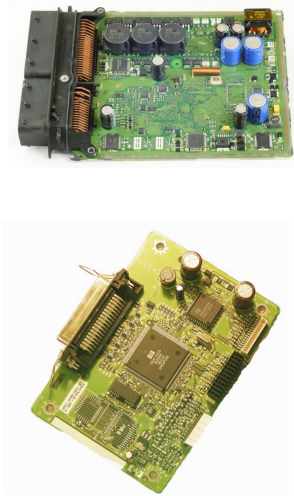
$$= \sqrt{(1\ \text{nW})(36\ \Omega)}$$

$$\approx 0.19\ \text{mV}$$

$I_{max} = 5.3\ \mu\text{A}$

Identifying Antennas

Good Antenna Parts		Poor Antenna Parts	
<100 MHz	>100 MHz	<100 MHz	>100 MHz
<p>Cables</p> <p>Metal Chassis or Enclosure</p>	<p>Tall Components or Heatsinks</p> <p>Seams in shielding enclosures</p> <p>Sparsely populated power planes</p>	<p>Integrated Circuits</p> <p>Microstrip or stripline traces</p> <p>Anything that is not big</p>	<p>Integrated Circuits</p> <p>Microstrip or stripline traces</p>



Free-space wavelength at 100 MHz is 3 meters

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Converting V_{CM} to CISPR 32 Maximum Radiated Emissions

$$\begin{aligned}
 V_{CM} &= \sqrt{P_{rad} R_{rad}} \\
 &= |E|_r \sqrt{\left(\frac{2\pi R_{rad}}{\eta D_0}\right)} \\
 &= |E|_r \sqrt{\left(\frac{2\pi(36\Omega)}{(377\Omega)1.6}\right)} \\
 &= 0.612 |E|_r
 \end{aligned}$$

At a 3-meter test distance:

$$V_{CM} \text{ (in microvolts)} \approx 1.9 |E| \text{ (in microvolts/meter)}$$

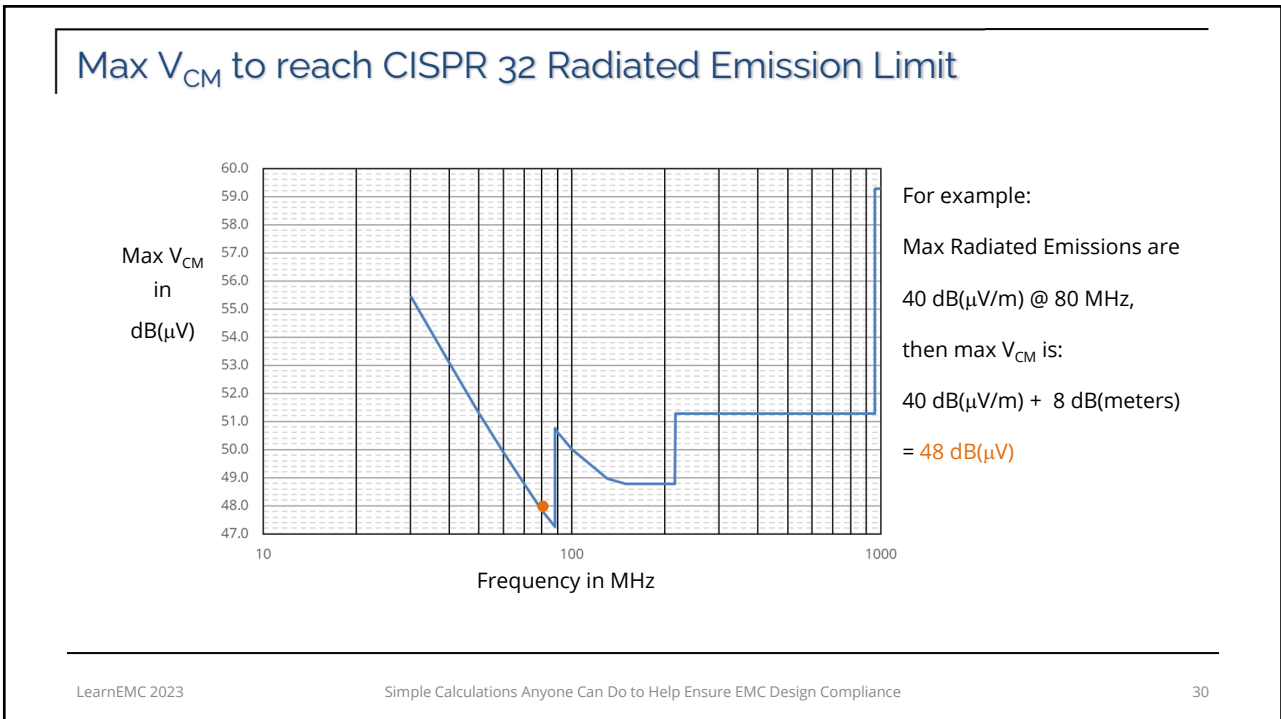
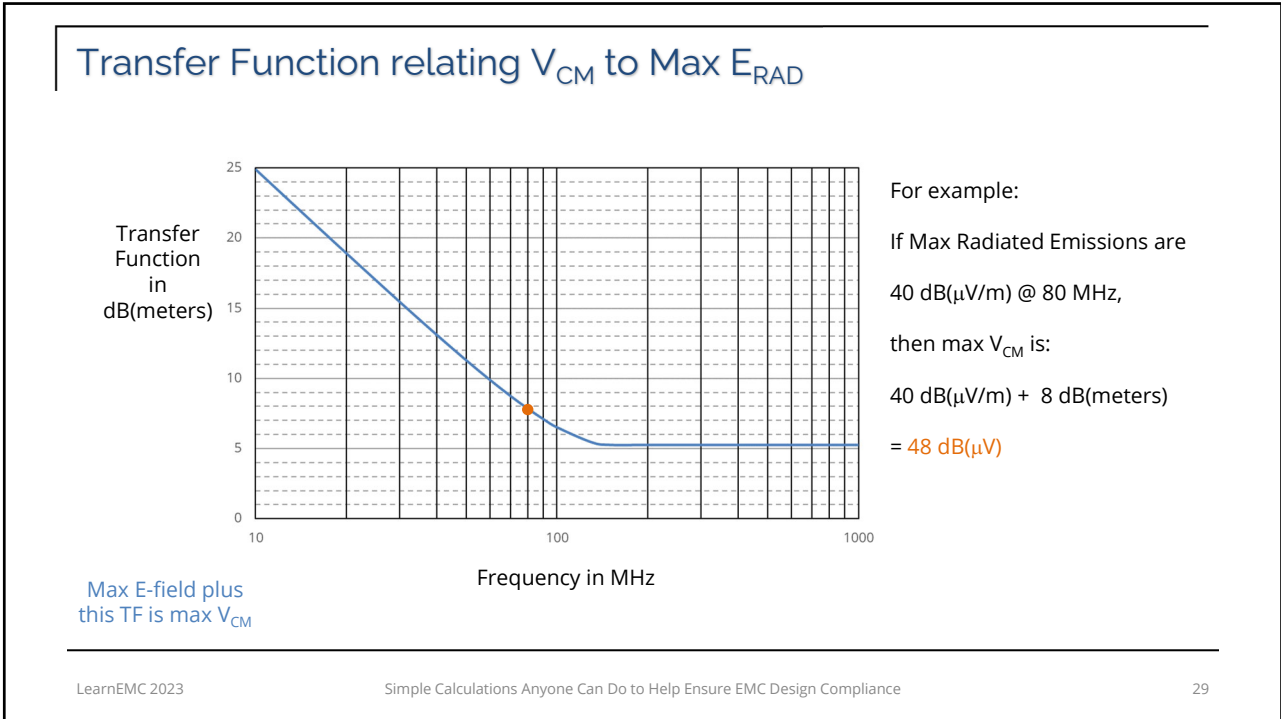
$$V_{CM} \text{ [in dB(\mu V)]} \approx |E| \text{ [in dB(\mu V/m)]} + 5.3 \text{ dB}_{(meters)}$$

But the vertical cable length is only 1 meter, so below 75 MHz the max radiated field is weaker.

$$\text{cable factor} = 20 \log \left(\sin \left(\frac{\pi \ell}{\lambda} \right) \right)_{\ell \leq \lambda/2}$$

S. Deng, T. Hubing and D. Beetner, "Estimating Maximum Radiated Emissions from Printed Circuit Boards with an Attached Cable," *IEEE Trans. on Electromagnetic Compatibility*, vol. 50, no. 1, Feb. 2008, pp. 215-218.
 C. Su and T. Hubing, "Improvements to a method for estimating the maximum radiated emissions from PCBs with cables," *IEEE Trans. on Electromagnetic Compatibility*, vol. 53, no. 4, Nov. 2011, pp. 1087-1091.

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Max V_{CM} to reach CISPR 25 Radiated Emission Limit

There is no good way to calculate this, because the voltage driving the harness is the source, but the radiating element depends on the test set-up.

One method:

- Total arc = 157 cm
- Wire to table = 5 cm
- Rest of field line = 152 cm
- Field strength of arc: $V_{CM}/1.52 \text{ m}$ or TF = 3.6 dB(m) – low estimate
- Using current distribution on plane: TF = 2.9 or 9.4 dB(m)

Another method:

40 dB(μV) \Rightarrow 24 dB($\mu\text{V}/\text{m}$)

TF = 16 dB(m)

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Do I need to match this Transmission Line?

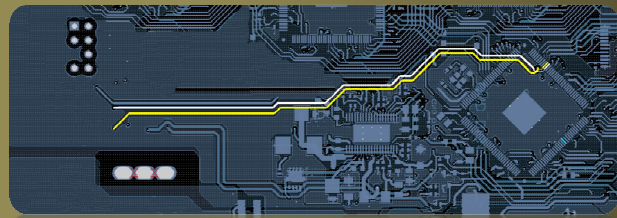
$$t_r < 2t_{PD}$$

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Remember!

- ❑ Don't use matched terminations and controlled impedance traces unless you are forced to!
 - ❖ by a signal specification (e.g., CAN, USB, HDMI)
 - ❖ by need to send very high-speed signals long distances (e.g., 100 Mbps > 10 cm)
- ❑ Instead, control ALL transition times so that $t_r > 2 * t_{PD}$.

Maximum Crosstalk Between Two Traces?



Crosstalk in Transmission Lines

- ❑ Crosstalk in Electrically Short Transmission Lines
 - ❖ Common-Impedance Coupling
 - ❖ Electric-Field Coupling
 - ❖ Magnetic-Field Coupling
- ❑ Crosstalk in Electrically Long Matched Transmission Lines

Definition of Crosstalk

Merriam-Webster: unwanted signals in a communication channel (as in a telephone, radio, or computer) caused by transference of energy from another circuit (as by leakage or coupling)

Crosstalk in Electrically Short Transmission Lines

❖ Common-Impedance Coupling

$$\text{XTALK(dB)} = 20 \log \left(\frac{V_{\text{SIG2}}}{V_{\text{SIG1}}} \Big|_{V_{S2}=0} \right)$$

Weak Coupling Assumption:

$$I_G \approx I_1$$

$$I_G \approx \frac{V_{\text{SIG1}}}{R_{L1}}$$

$$V_G = I_G R_G \approx V_{\text{SIG1}} \frac{R_G}{R_{L1}}$$

$$V_{\text{SIG2}} = V_G \left(\frac{R_{L2}}{R_{S2} + R_{L2}} \right) \approx V_{\text{SIG1}} \frac{R_G}{R_{L1}} \left(\frac{R_{L2}}{R_{S2} + R_{L2}} \right)$$


Trace Width: 56 mils
Trace Height: 32 mils
Plane Width: 56 mm
Plane Length: 204 mm
Copper Thickness: 0.5 oz.

$$R_G = \frac{\ell}{\sigma A} = \frac{0.204 \text{ m}}{(5.7 \times 10^7 \text{ S/m})(0.056 \text{ m} \times 17.4 \times 10^{-6} \text{ m})} = 3.7 \times 10^{-3} \Omega$$

$$\begin{aligned} \text{XTALK}_{\text{FE}} &= 20 \log \left[\frac{R_G}{R_{L1}} \left(\frac{R_{L2}}{R_{S2} + R_{L2}} \right) \right] \\ &= 20 \log \left[\frac{3.7 \times 10^{-3}}{50} \left(\frac{50}{50 + 50} \right) \right] \\ &= 20 \log [36.7 \times 10^{-6}] \\ &= -89 \text{ dB} \end{aligned}$$

Crosstalk in Electrically Short Transmission Lines

❖ Electric-Field Coupling

$$\text{XTALK(dB)} = 20 \log \left(\frac{V_{\text{SIG2}}}{V_{\text{SIG1}}} \Big|_{V_{S2}=0} \right)$$

Weak Coupling Assumption: $I_{C12} \ll I_1$

$$|V_{\text{SIG2}}| = \left| V_{\text{SIG1}} \frac{R_{S2} \parallel R_{L2}}{(R_{S2} \parallel R_{L2}) - (j/\omega C_{12})} \right|$$

$$\approx V_{\text{SIG1}} (R_{S2} \parallel R_{L2}) \omega C_{12}$$

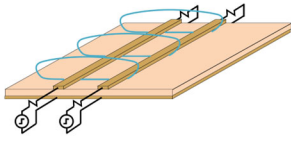

Trace Width: 1.4 mm (56 mils)
Trace Height: 0.81 mm (32 mils)
Trace Separation: 1 mm (40 mils)
Parallel Trace Length: 15 cm (5.9 in)
Dielectric ϵ_r : 4.2
 C_{12} : 2.84 pF
 C_{11} : 15 pF

$$\begin{aligned} \text{XTALK}_{10\text{MHz}} &= 20 \log [\omega (R_{S2} \parallel R_{L2}) C_{12}] \\ &= 20 \log [(2\pi \times 10^7 \text{ Hz})(50 \Omega \parallel 50 \Omega)(2.84 \times 10^{-12} \text{ F})] \\ &= 20 \log [4.46 \times 10^{-3}] \\ &= -47 \text{ dB} \end{aligned}$$

Note: At 10 kHz, XTALK would be 60 dB lower. Common impedance coupling would dominate.

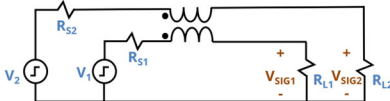
Crosstalk in Electrically Short Transmission Lines

❖ Magnetic-Field Coupling



$$XTALK(dB) = 20 \log \left(\frac{V_{SIG2}}{V_{SIG1}} \Big|_{V_{S2}=0} \right)$$

Weak Coupling Assumption: $I_2 \ll I_1$



$$|V_{SIG2}| \approx \left(\frac{V_{SIG1}}{R_{L1}} \right) \omega L_{12} \left(\frac{R_{L2}}{R_{S2} + R_{L2}} \right)$$



Trace Width: 1.4 mm (56 mils)
 Trace Height: 0.81 mm (32 mils)
 Trace Separation: 1 mm (40 mils)
 Parallel Trace Length: 15 cm (5.9 in)
 L_{12} : 16.4 nH
 L_{11} : 43.4 nH

$$\begin{aligned} XTALK_{10MHz} &= 20 \log \left[\left(\frac{\omega L_{12}}{R_{L1}} \right) \left(\frac{R_{L2}}{R_{S2} + R_{L2}} \right) \right] \\ &= 20 \log \left[\left(\frac{2\pi \times 10^7 \text{ Hz} (16.4 \times 10^{-9} \text{ H})}{50 \Omega} \right) \left(\frac{50 \Omega}{50 \Omega + 50 \Omega} \right) \right] \\ &= 20 \log [1.03 \times 10^{-2}] \\ &= -39.7 \text{ dB} \end{aligned}$$

Crosstalk in Electrically Short Transmission Lines

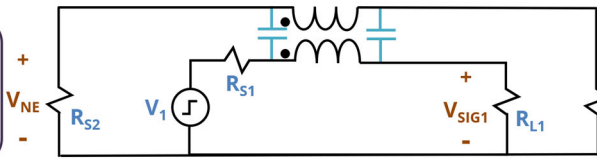
❖ Near-End and Far-End Coupling



Trace Width: 56 mils
 Trace Height: 32 mils

For $V_{SIG1} = 1 \text{ V}$
 Electric-Field Coupling Voltage: 4.46 mV
 Magnetic-Field Coupled Voltage: 10.3 mV

$$\begin{aligned} V_{NE} &= 4.46 + 10.3 \\ &= 14.8 \text{ mV} \\ XTALK_{NE} &= -36.7 \text{ dB} \end{aligned}$$



$$\begin{aligned} V_{FE} &= 4.46 - 10.3 \\ &= -5.84 \text{ mV} \\ XTALK_{FE} &= -44.7 \text{ dB} \end{aligned}$$

Crosstalk in Electrically Short Transmission Lines

- ❖ Time-Domain Coupling (weak coupling assumption)

Common-Impedance Coupling: $V_{SIG2} \approx V_{SIG1} \frac{R_G}{R_{L1}} \left(\frac{R_{L2}}{R_{S2} + R_{L2}} \right)$ (same as frequency domain)

Electric-Field Coupling: $V_{SIG2} \approx (R_{S2} \parallel R_{L2}) C_{12} \left(\frac{\partial V_{SIG1}}{\partial t} \right)$

Magnetic-Field Coupling: $V_{SIG2} \approx - \left(\frac{\partial V_{SIG1}}{\partial t} \right) \frac{L_{12}}{R_{L1}} \left(\frac{R_{L2}}{R_{S2} + R_{L2}} \right)$ (far end)

$V_{SIG2} \approx + \left(\frac{\partial V_{SIG1}}{\partial t} \right) \frac{L_{12}}{R_{L1}} \left(\frac{R_{S2}}{R_{S2} + R_{L2}} \right)$ (near end)

The plots show a trapezoidal signal V1 on the top plot. Below it, three plots show the resulting crosstalk V12: 1) Common-impedance coupling (orange) shows a smaller trapezoidal pulse. 2) Electric-field coupling (red) shows sharp spikes at the leading and trailing edges of V1. 3) Magnetic-field coupling (blue) shows sharp spikes at the leading and trailing edges of V1, with opposite polarity to the electric-field coupling.

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Crosstalk in Longer Transmission Lines

- ❖ First null in near-end crosstalk occurs when line is one half-wavelength long

$|V_{SIG2}|_{FE} \approx V_1 \left(\frac{(R_{S2} \parallel R_{L2}) C_{12}}{\sqrt{\mu\epsilon}} \right) |e^{j\beta l} - 1|$

The graph shows crosstalk in dB on the y-axis (from -80 to 0) and frequency in MHz on the x-axis (log scale from 0.1 to 1000). The curve rises linearly until about 100 MHz, then exhibits a series of oscillations (nulls and peaks) that dampen in amplitude as frequency increases.

$|V_{SIG2}|_{NE} \approx V_1 \left(\frac{(R_{S2} \parallel R_{L2}) C_{12}}{2\sqrt{\mu\epsilon}} \right) |e^{j2\beta l} - 1|$

The graph shows crosstalk in dB on the y-axis (from -80 to 0) and frequency in MHz on the x-axis (log scale from 0.1 to 1000). The curve rises linearly until about 100 MHz, then exhibits a series of oscillations. A vertical red line is drawn at approximately 100 MHz, with a label 'RFLC for matched case' pointing to it.

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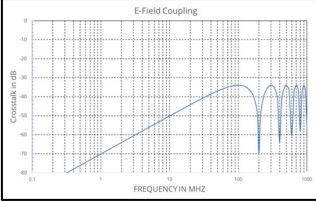
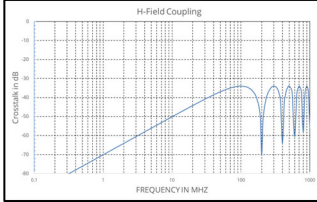
Crosstalk in Longer Transmission Lines

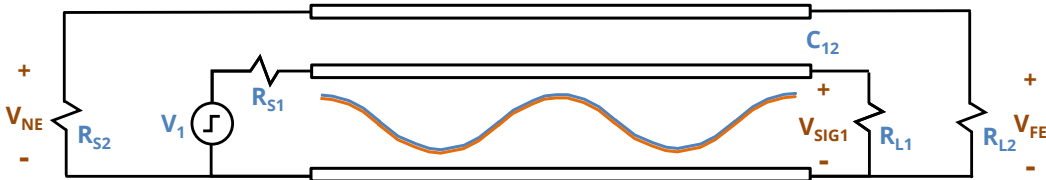
❖ Total-Field Coupling, frequency domain

For matched transmission lines in a homogeneous dielectric,

- ❑ Far-end E-field and H-field coupling are equal and 180° out of phase.
- ❑ Near-end E-field and H-field coupling are equal and in phase.

$$\begin{pmatrix} C_{12} \\ C_{11} \end{pmatrix} = \begin{pmatrix} L_{12} \\ L_{11} \end{pmatrix} \text{ for matched case}$$

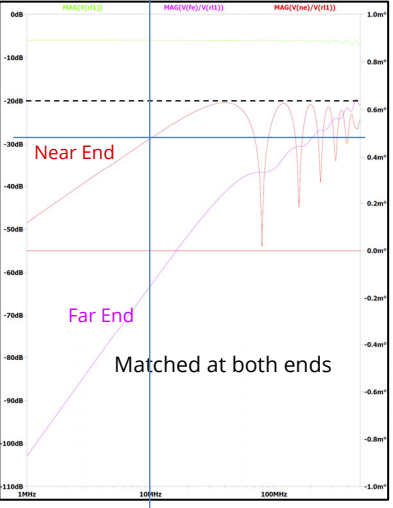


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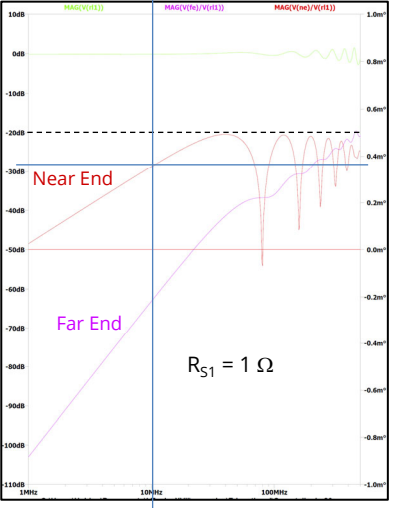
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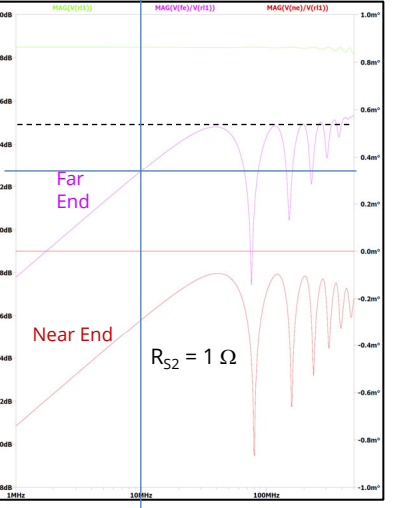
LTSpice Simulations



Matched at both ends



$R_{S1} = 1 \Omega$

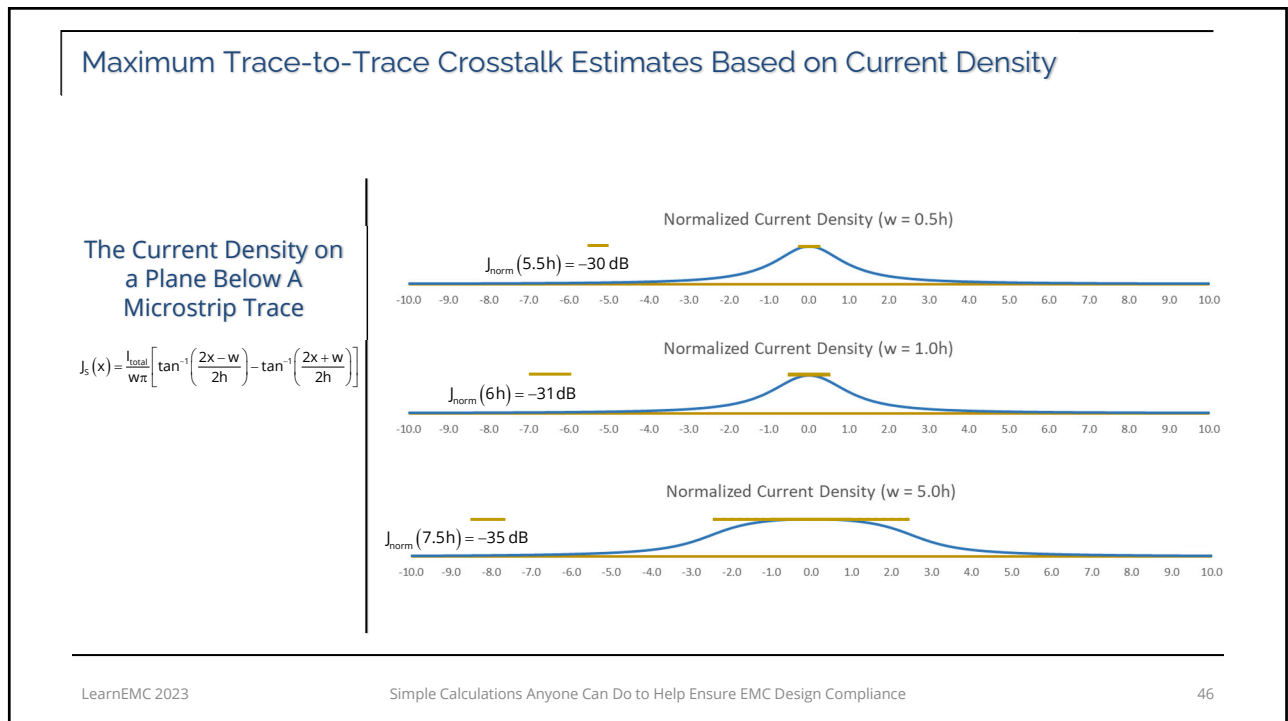
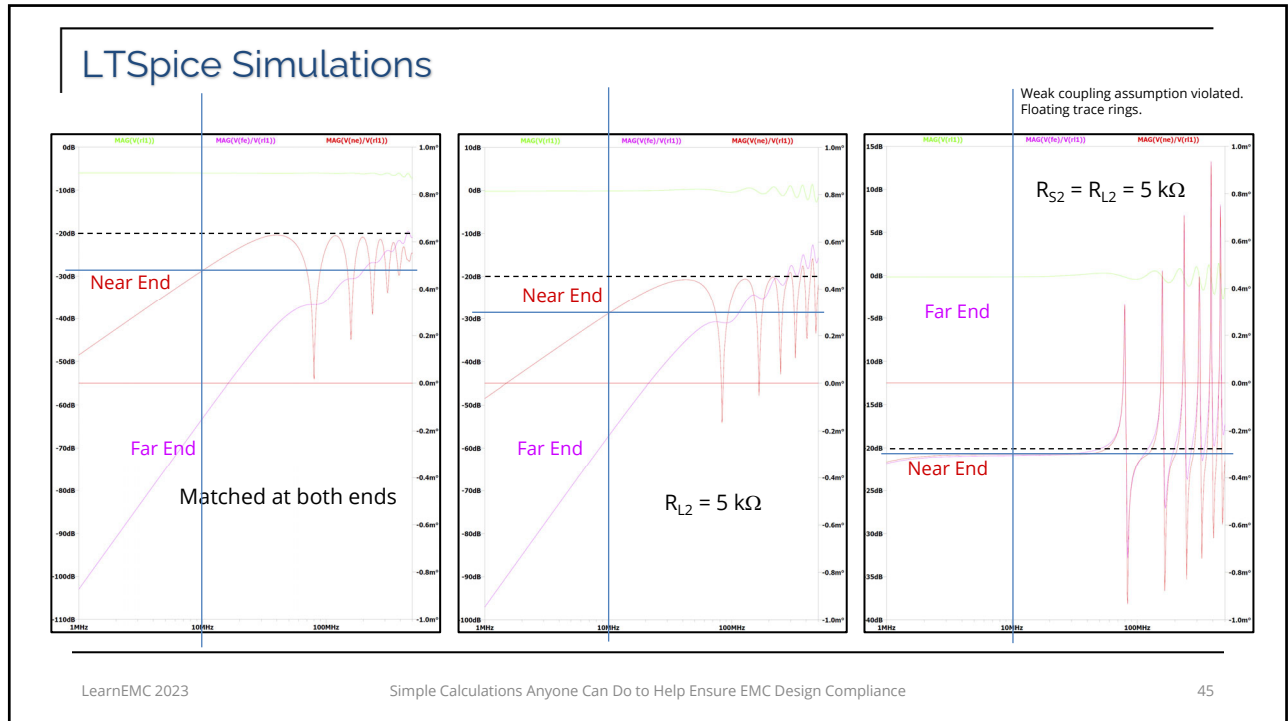


$R_{S2} = 1 \Omega$

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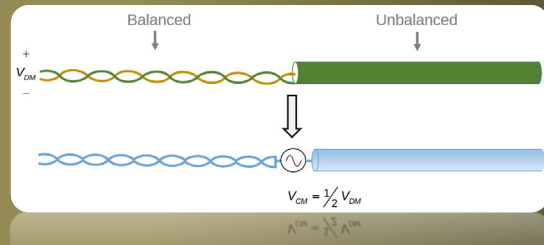
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Differential-Mode to Common-Mode Conversion

$$V_{CM} = \Delta h \times V_{DM}(x)$$

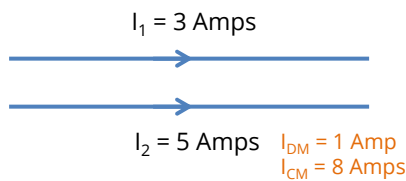


Common-mode and Differential-mode Current

General Definition

$$I_{DM} = (1-h)I_1 - hI_2$$

$$I_{CM} = I_1 + I_2$$



For Balanced Pairs

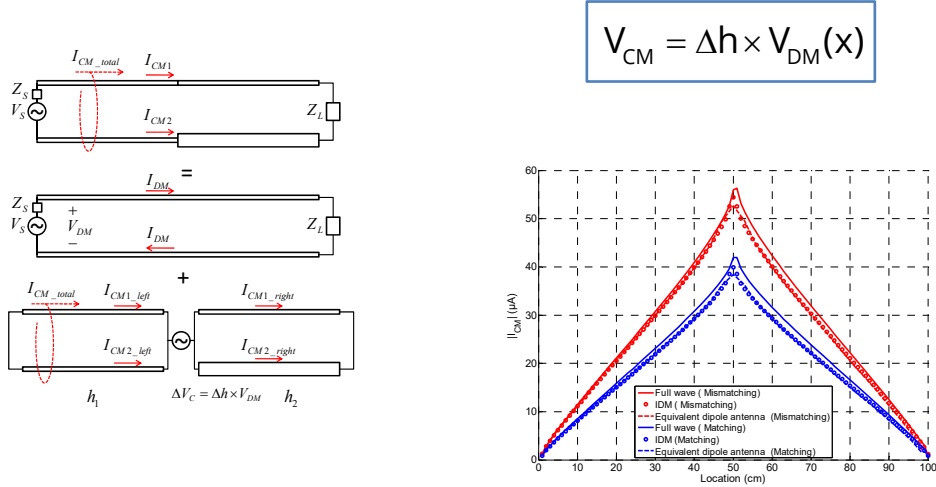
$$I_{DM} = \frac{I_1 - I_2}{2}$$

$$I_{CM} = I_1 + I_2$$

$$I_1 = I_{DM} + hI_{CM} \quad \text{AND} \quad I_{DM} = \frac{V_{DM}}{Z_{DM}}$$

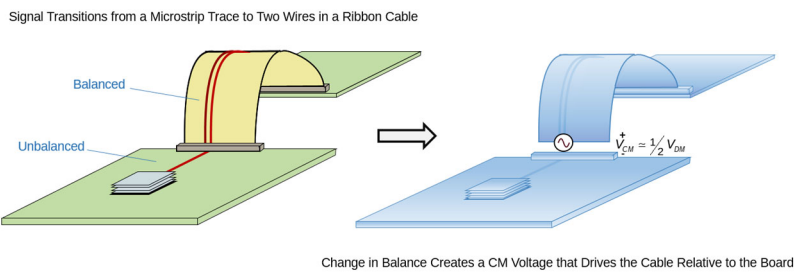
$$I_2 = -I_{DM} + (1-h)I_{CM}$$

The Imbalance Difference Model



Driving a Ribbon Cable

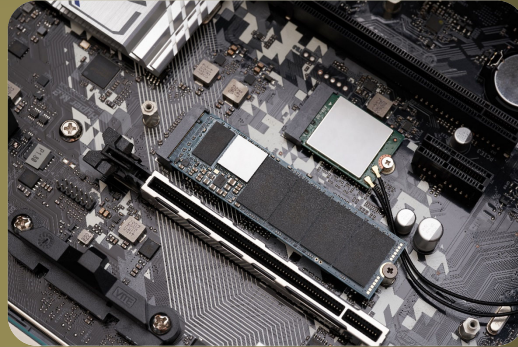
Imbalance difference modeling accurately determines the common-mode voltage driving a cable due to changes in electrical balance.



Single-ended sources should use unbalanced transmission lines and unbalanced terminations!

Electric Field Coupling

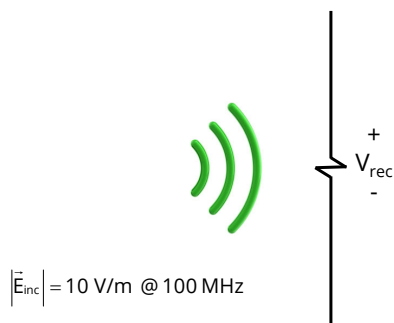
$$V_{\text{rec-max}} \approx L_{\text{antenna}} E_{\text{inc}}$$



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Quantifying Electric Field Coupling

Electric Field Coupling



For matched resonant dipole:

$$P_{\text{rec}} = \frac{|10 \text{ V/m}|^2}{377 \Omega} = 0.26 \text{ W/m}^2$$

$$A_e = \frac{(3)^2}{4\pi} 1.64 = 1.17 \text{ m}^2$$

$$P_{\text{rec}} = (0.26 \text{ W/m}^2)(1.17 \text{ m}^2)(1) = 0.30 \text{ W}$$

$$V_{\text{rec}} = \sqrt{R P_{\text{rec}}} = \sqrt{(72 \Omega)(0.30 \text{ W})} = 4.7 \text{ V}$$

Note: The open-circuit voltage would be approximately twice this value or 10 volts.

Useful Approximate Solution:

$$V_{\text{rec-max}} \approx L_{\text{antenna}} E_{\text{inc}}$$

$$V_{\text{rec-max}} \approx 1.5 \text{ m}(10 \text{ V/m}) = 15 \text{ V}$$

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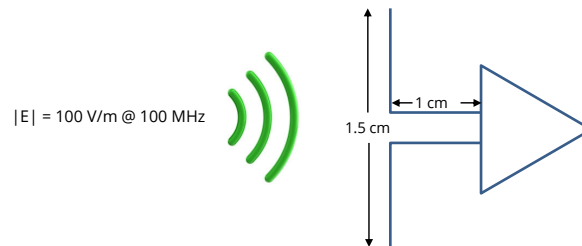
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Basic Calculations

H-field Coupling from Plane Wave to Electrically Small Circuit

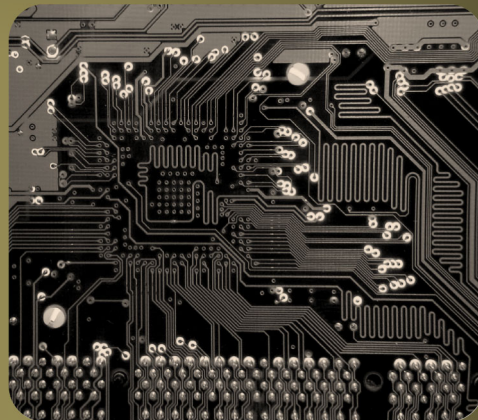
The magnetic field from a 100 V/m plane wave couples to a 3-cm dipole op-amp circuit. Modeling the input to the op-amp as an infinite impedance, calculate the maximum voltage developed across the input.



$$V_{\text{rec-max}} \approx (0.015 \text{ m})(100 \text{ V/m}) = 1.5 \text{ V}$$

Magnetic Field Coupling

$$|V_{\text{loop}}| = \left| \frac{d\Psi}{dt} \right| = \omega \Psi \approx 2\pi f \mu_0 |H| \times (\text{loop area})$$



Quantifying Magnetic Field Coupling

Magnetic Field Coupling

Current I flowing in a wire

$\vec{H} = \frac{I}{2\pi r} \hat{\phi}$

V_{rec}

For a 3 cm x 3 cm loop located 10 cm from a 1-amp current @ 20 kHz:

$$|\vec{H}| = \frac{(1\text{ A})}{2\pi(0.1\text{ m})} = 1.6\text{ A/m}$$

$$V_{loop} = 2\pi f \mu_0 |\vec{H}| (\text{loop area})$$

$$= 2\pi(2 \times 10^4\text{ Hz})(4\pi \times 10^{-7}\text{ H/m})(1.6\text{ A/m})(0.03\text{ m})^2$$

$$= 230\text{ }\mu\text{V}$$

$$|V_{loop}| = \left| \frac{d\Psi}{dt} \right| \qquad V_{rec} = V_{loop} \frac{R_{load}}{Z_{loop}}$$

$$= \omega \Psi \qquad = V_{loop} \frac{R_{load}}{R_{load} + j\omega L_{loop}}$$

$$\approx 2\pi f \mu_0 |\vec{H}| \times (\text{loop area})$$

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Basic Calculations

H-field Coupling from Plane Wave to Electrically Small Circuit

The magnetic field from a 100 V/m plane wave couples to a 3 cm x 1 cm op-amp circuit. Modeling the input to the op-amp as an infinite impedance, calculate the maximum voltage developed across the input.

$|E| = 100\text{ V/m @ } 100\text{ MHz}$

$$|\vec{H}| = \frac{|E|}{\eta_0} = \frac{100\text{ V/m}}{377\Omega} = 0.265\text{ A/m}$$

$$|V_{loop}| = \omega \Psi$$

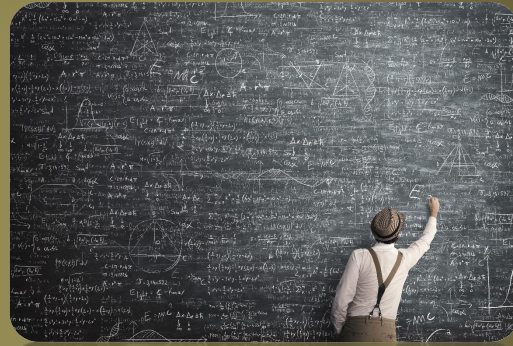
$$= \omega \mu_0 |\vec{H}| A$$

$$= 2\pi(100 \times 10^6\text{ Hz})(4\pi \times 10^{-7}\text{ H/m})(0.265\text{ A/m})(0.03 \times 0.01\text{ m}^2)$$

$$= \mathbf{63\text{ mV}}$$

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Summary



Summary

- Maximum emissions and maximum coupled interference voltages can always be calculated.
- The preciseness and usefulness of these calculations depends on the quality of the information provided.
- Relatively simple calculations can take a lot of the guesswork out of product design for guaranteed compliance.

